

Electronic Acknowledgement Receipt

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Application Number:	10512405
International Application Number:	
Confirmation Number:	4607
Title of Invention:	HIGH RESISTANCE SILICON WAFER AND ITS MANUFACTURING METHOD
First Named Inventor/Applicant Name:	Nobumitsu Takase
Customer Number:	25227
Filer:	Bradley J. Meier/Natherine Oranza
Filer Authorized By:	Bradley J. Meier
Attorney Docket Number:	474082002800
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Submitted with Payment	yes
Payment Type	Deposit Account
Payment was successfully received in RAM	\$1758
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Issue Fee Payment (PTO-85B)	474082002800IF.pdf	91032 6a8079370d8d1bcb4c1c5023e608e653 5423800	no	1

Warnings:

Information:

2	Fee Worksheet (PTO-06)	fee-info.pdf	8505 6a83e1b675a73ccdfc74073344c483f c38132	no	2
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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